

PATENT NUMBER

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PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/752998	F	324	705	3839	KARLSEN


APPLICANTS

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TITLE

Method and apparatus for testing semiconductor devices using the back side of a circuit board

PTO-204
1289[illegible]

 TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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